



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Cheng-Lien Chiang
Assignee: Bridge Semiconductor Corporation
Title: METHOD OF MAKING A SEMICONDUCTOR PACKAGE
DEVICE THAT INCLUDES A CONDUCTIVE TRACE WITH
RECESSED AND NON-RECESSED PORTIONS
Serial No.: 10/059,686 Filed: January 29, 2002
Examiner: Zarneke, D. Group Art Unit: 2827
Atty. Docket No.: BDG005-1

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AFTER ALLOWANCE

Please amend the application as follows.

The Abstract Amendments begin at page 2.

The Specification Amendments begin at page 3.

The Claim Amendments begin at page 4.

The Remarks begin at page 20.

The Appendix with amended drawing figures is attached following page 25.